

Over Voltage and Over Current Protector

Description

SA8206 can disconnect the systems from its output pin (OUT) in case wrong input operating conditions are detected. The system is positive overvoltage protected up to 36V.

The internal over voltage thresholds (OVLO) is 6.0V and internal over current thresholds (OCP) is 2.5A.

SA8206 also has internal over temperature protect (OTP) function and it can monitor chip temperature to protect the device.

The device is packaged in advanced full-Green Packaging (SOT-23).

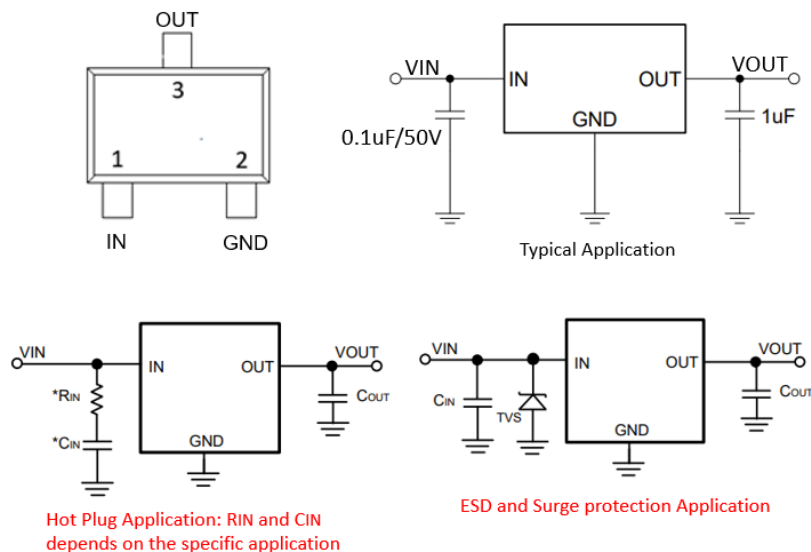
Features

- Typical Ron: 140mΩ@1.0A
- VIN Operating Range: 2.5 to 36V
- Internal Overvoltage Lockout: 6.0V
- Internal Overcurrent Lockout: 2.5A
- OVP Response time: 50ns (typ.)
- Output Discharge
- Startup Debounce Time: 8ms
- Output Power on time: 8ms
- Internal OTSD Protection
- SOT-23 Package

Applications

- GPS
- MID
- SLR Digital Cameras
- Industrial Handheld and Enterprise Equipment

SA8206 Package & Simplified Schematic

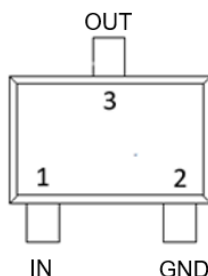


Device Information

Part No.	Package	Quantity	Operation Temp.
SA8206	SOT-23	3000	-20~85 °C

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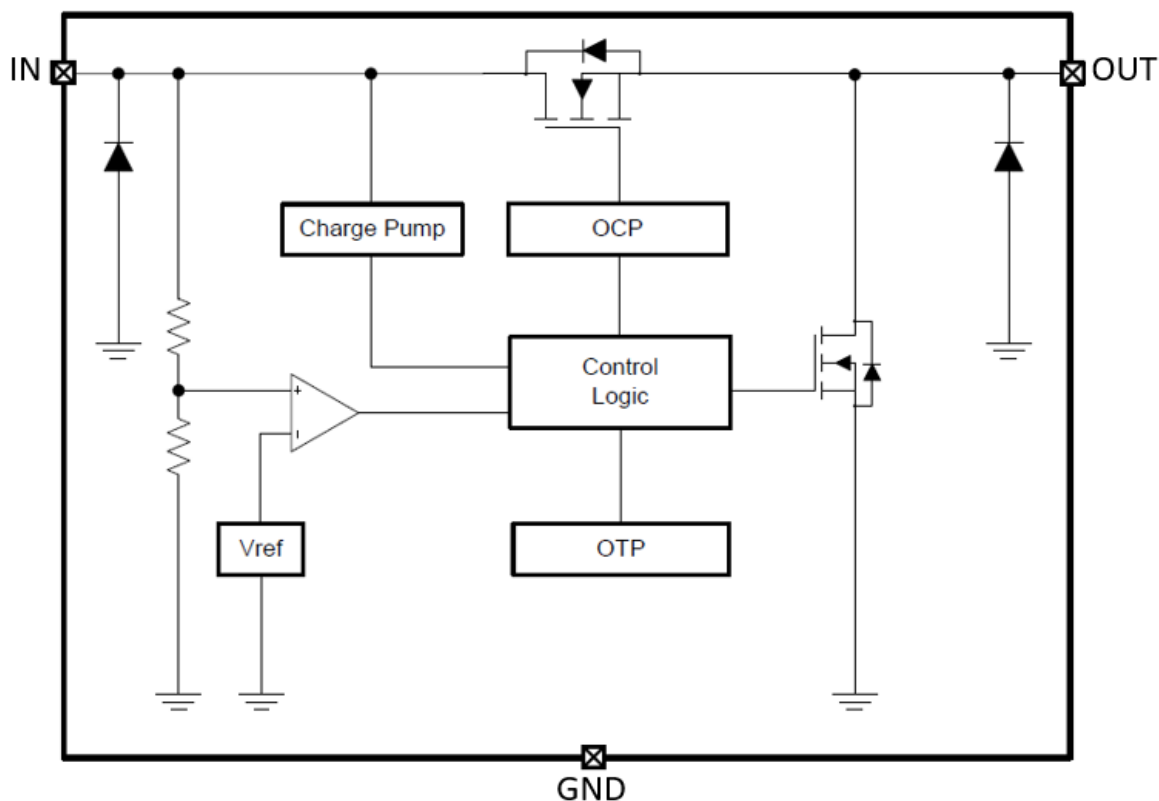
Pin Descriptions



NAME	SOT-23	TYPE	DESCRIPTION
IN	1	P	Input pin, connect to AC adaptor or VBUS. A 0.1~1uF low ESR ceramic capacitor or larger must be connected as close as to this pin. It is recommended to use 50V capacitor or according to application.
GND	2	P	Power ground
OUT	3	O	Output pin, Connect to load.

(1) Directions: I = input, O = output, OZ = tri-state output, OD = open-drain output, IO = input/output

Function Block Diagram



Over Voltage and Over Current Protector

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$)

Parameter		Min.	Max.	Unit
Input voltage (IN pin)	VIN	-0.3	36	V
Output voltage (OUT pin)	VOUT	-0.3	7.0	V
Power dissipation	P _D		0.5	W
Thermal resistance	SOT23-6 θ_{JA}	260		$^{\circ}\text{C/W}$
ESD(HBM)		2.0		kV
Operation temp.	T _J	-40	150	$^{\circ}\text{C}$
Storage temp.	T _{stg}	-65	150	$^{\circ}\text{C}$

Recommended operating conditions ($T_A=25^{\circ}\text{C}$)

Parameter		Min.	Max.	Unit
Input voltage (IN pin)	VIN	3.0	28.0	V
Output voltage (OUT pin)	VOUT	0	5.5	V
Output Contiguous Current	I _{OUT}	0	1.5*	A

Notes: * Using 25mm² FR4 Signal layer PCB (1 oz) under VM=5.0V test.

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Electrical Characteristics ($V_{IN}=5V$, $T_A=25^\circ C$, $R_{LOAD}=20\Omega$)

over operating free-air temperature range (unless otherwise noted)

Parameter		Test Condition	Min.	Typ.	Max.	Unit
DC characteristics and Power-ON-Reset						
VIN operating voltage	V_{IN}		2.5		36	V
VIN operating supply current	I_{VIN_ON}	$V_{IN}=5V$		160	250	uA
VIN-to-VOUT ON resistance	R_{ON}	$V_{IN}=5V$, $I_{OUT}=0.1A$		130		m Ω
		$V_{IN}=5V$, $I_{OUT}=1.0A$		140		m Ω
Output discharge resistance	R_{DIS}			500		Ω
VIN undervoltage lockout	V_{UVLO_R}	VIN rising		2.20		V
	V_{UVLO_F}	VIN falling		1.90		V
Output power on time	T_{ON}	VIN = 0 -> 5V to output ON		8		ms
Input Over-Voltage Protection (OVP)						
VIN OVLO Protection	V_{OVLO_R}		5.7	6.0	6.3	V
Input low level voltage	V_{OVLO_HYS}			150		mV
OVP active time	T_{OVP}	VIN = 5 -> 10V with rising 1V/20ns		50		ns
OVP recovery time	T_{ON_OVP}	VIN = 10 -> 5V to output ON		8		ms
Input Over-Current-Protection (OCP)						
OCP threshold	I_{OCP}		2.0			A
Over-Temperature-Protection (OTP)						
OTP threshold	T_{OTP}			165		$^\circ C$
OTP threshold hysteresis	T_{OTP_HYS}			40		$^\circ C$

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Functional Description

The OVP switch with overvoltage protection feature a low 140m Ω (typical) on-resistance (RON) internal FET and protect low-voltage systems against voltage faults up to 36VDC. If the input voltage (VIN) exceeds 6.0V, or input current exceeds 2.5A, the internal FET is quickly turned off to prevent damage to the protected downstream components.

The internal FET turns off when the junction temperature exceeds +165°C (TYP.). The device exits thermal shutdown after the junction temperature cools by 40°C (TYP.).

Input Capacitor

To limit the voltage drop on the input supply caused by transient inrush current when the switch turns on into a discharged load capacitor or short-circuit, a capacitor 0.1-1 μ F or larger must be placed between the VIN and GND pins.

Output Capacitor

A1 μ F or larger capacitor should be placed between the OUT and GND pins.

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Package SOT-23

